

Oven Controlled Crystal Oscillator

DLC117



Actual size



■ Features

- Small size OCXO
25.4(L)×25.4(W)×13.2(H)mm
- Low phase noise
- Extra long-term stability
- Adopt SC-Cut resonator



RoHS Compliant

■ Applications

- Base station, Measuring equipment

■ Standard Specification

Item	Type	Legend	DLC117
Output Specification		—	C-MOS
Output Frequency Range		f_o	10 to 20MHz (10.000MHz Standard Specification)
Supply Voltage		V _{CC}	+3.3V±0.165V/+5.0V±0.25V
Frequency Control Voltage		V _{cont}	+1.65V±1.65V/+2.5V±2.5V
Operating Temperature Range		T _{use}	-20 to +70 °C
Frequency Stability		—	±100×10 ⁻⁹ max.
Tolerance			
vs. Temperature		f_o -Tc	±20×10 ⁻⁹ max. [-20 to +70°C]
vs. Supply Voltage		f_o -V _{CC}	±5×10 ⁻⁹ max. [+3.3V±5%/+5.0V±5%]
vs. Aging		f_{age}	±100×10 ⁻⁹ max./year
Frequency Adjustment Range		f_{cont}	±0.5×10 ⁻⁶ min. [+1.65±1.65V/+2.5±2.5V]
Power Consumption		—	3W max. [Warm-up] 1W max. [Steady States]
Output Load		L _{CMOS}	15pF±10%
Symmetry		SYM	45 to 55% [50% V _{CC} Level]
0 Level Output Voltage		V _{OL}	V _{CC} ×0.1 max.
1 Level Output Voltage		V _{OH}	V _{CC} ×0.9 min.
Rise and Fall Time		tr, tf	15ns max.
Phase Noise			
Offset 10Hz		—	-130dBc/Hz typ.
Offset 100Hz			-142dBc/Hz typ.
Offset 1kHz			-148dBc/Hz typ.
Offset 10kHz			-152dBc/Hz typ.

Moisture prevention packing is unnecessary.
Moisture Sensitivity Level : Level 1 (IPC/JEDEC J-STD-033)

Consult our sales representative for other specifications.

[mm]

■ Dimensions

