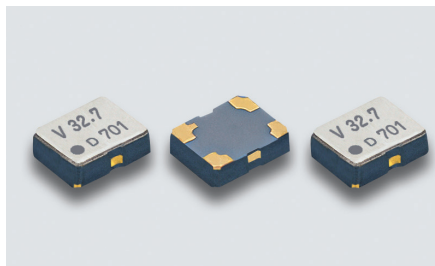


SMD TCXO

DSK1612ATD



Actual size □

■ Features

- Digital temperature compensated type
- High precision : $\pm 5.0 \times 10^{-6}$ (-40 to +85°C)
- Low current consumption
- CMOS level output

■ Applications

- High precision clock source
- High precision clock source for RTC



Pb-Free



RoHS Compliant

■ Standard Specification

Item	Legend	Spec.				Condition
		min.	typ.	max.	Unit	
Output Frequency	f_0	—	32.768	—	kHz	
Supply Voltage Range	V _{CC}	+1.5	—	3.63	V	Temperature Compensated Operating
Frequency Tolerance	f_{tol}	-5.0	—	+5.0	$\times 10^{-6}$	V _{CC} =+1.8V or +3.3V, T _A =-40 to +85°C (Standard operating temperature range, Referenced to 32.768kHz)
Current Consumption	I _{CC1}	—	0.90	1.90	μA	V _{CC} =+1.8V, T _A =-40 to +85°C, at No Load (1)
		—	1.23	2.60		V _{CC} =+3.3V, T _A =-40 to +85°C, at No Load (1)
	I _{CC2}	—	1.26	2.43		V _{CC} =+1.8V, T _A =-40 to +85°C, at No Load Temperature Compensation Interval: 0.5s (standard specification) (2)
		—	1.59	3.12		V _{CC} =+3.3V, T _A =-40 to +85°C, at No Load Temperature Compensation Interval: 0.5s (standard specification) (2)
Symmetry	SYM	40	50	60	%	at 50% V _{CC}
0 Level Output Voltage	V _{OL}	—	—	V _{CC} ×0.1	V	
1 Level Output Voltage	V _{OH}	V _{CC} ×0.9	—	—	V	
Rise and Fall Time	t _r , t _f	—	—	40	ns	10 to 90% V _{CC} Level
Load Condition	L _{CMOS}	—	—	15	pF	
Start Up Time	T _{start}	—	—	0.5	s	
Packing Unit (3)		3000pcs./reel (φ180)				

(1) I_{CC1} is the current value when the temperature compensation circuit is not operating.

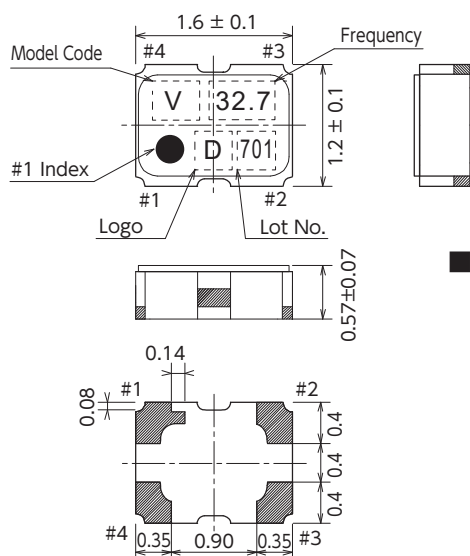
Consult our sales representative for other specifications.

(2) I_{CC2} is the average current value when the temperature compensation circuit is operating and non-operating.

(3) Moisture prevention packing is unnecessary. Moisture Sensitivity Level : Level1 (IPC/JEDEC J-STD-033)

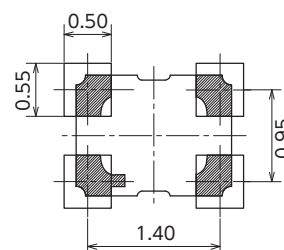
[mm]

■ Dimensions



■ Recommended Land Pattern

<Top View>



■ Current profile

