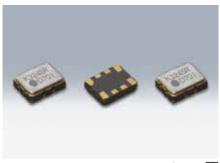


RoHS/ELV Compliant

SMD Real Time Clock Module (For Automotive)

DSK324SR



Actual size

■ Features

- Digital temperature compensated type High precision: $\pm 5.0 \times 10^{-6}$ (-40 to +85°C) $\pm 3.8 \times 10^{-6}$ (-10 to +60°C)
- Low current consumption
- Low voltage operation:
- +2.0 to +5.5V(Temperature Compensated Operating) +1.3 to +5.5V(Clock Timing Operating) 1²C-BUS serial interface:400kHz fast-mode compatible
- Clock function: hour minute second, Calendar function with auto leap year adjustment:year-month-day-day of week Alarm interrupt function:day-day of week-hour-minute
- Fixed-cycle timer interrupt function: $244 \mu s$ to 255 min
- Time update interrupt function:minute-second
- Clock output function: 32.768kHz, 1024Hz, 32Hz, 1Hz
- Supply voltage detection function:
- +2.0V temperature compensation operating voltage detection. +1.5V supply voltage undervoltage detection
- AEC-Q100/ÁEC-Q200 Compliant

Applications

ision clock source

"I²C-BUS" is a registered trademark of NXP Semiconductor

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■ Standard Specification			0 1			TO DOD IS A TOBISCO CARGONIAN OF THE SCHOOL COLUMN
ltem	Legend -	Spec.		Unit	Condition	
		min.	typ.	max.	Unit	Condition
Output Frequency	fo	_	32.768	_	kHz	
Supply Voltage Range	Vcc	+1.3	_	+5.5		(Clock Timing Operating)
	Vtem	+2.0	_	+5.5	V	(Temperature Compensated Operating)
	Vint	+1.5	_	+5.5		(Interface Operation) I ² C-BUS
Frequency Tolerance	f tol	-5.0	_	+5.0	×10 ⁻⁶	-40 to +85℃
	1_101	-3.8	_	+3.8		−10 to +60°C
Current Consumption	lcc1	_	+0.6	+2.0	μΑ	Vcc=+3.0V, Temperature Compensation Interval:30s, SCL=SDA=INTN=Vcc,CLKOE=GND (Output Off)
	lcc2	-	+1.5	+4.0	μΑ	Vcc=+3.0V, Temperature Compensation Interval:30, No Load, SCL=SDA=INTN=CLKOE=Vcc(Output On)
Load Condition	L_cmos	_	_	15	pF	
Start Up Time	-	_	_	1.0		Ta=+25℃, Vcc=+1.3V
	Tstart	_	_	3.0		Ta=-40 to +85°C, Vcc=+1.3 to +5.5V
	VDET1(1)	+1.8	+1.9	+2.0	V	Temperature Compensated Operation Detection Voltage
	VDET2(2)	+1.3	+1.4	+1.5		Power Supply Undervoltage Detection
Reliability						AEC-Q100/AEC-Q200
Packing Unit (3)						2000pcs./reel (\$\phi\$180)

- (1) When Vcc falls below VDET1, the internal detection circuit operates, and the intermittent temperature compensating stops. At the same time, the current temperature compensating data value is retained. When Vcc rises above VDET1 again, the intermittent temperature compensating is enabled.
- The Detection circuit operates at the temperature compensation interval. Moisture prevention packing is unnecessary.

 Moisture Sensitivity Level: LEVEL 1 (IPC/JEDEC J-STD-033)

Description

Consult our sales representative for other specifications.

Pin No.	Name	1/0	Function
#1	OE	I	Output control enable input (L:High impedance, H:Clock output)
#2	INTN	0	1Hz signal, alarm interrupt signal, fixed-cycle timer interrupt signal, and time update interrupt signal, Nch open-drain output.
#3	N.C.	_	None connection
#4	GND	_	Ground connection.
#5	Output	0	Clock output connection.
#6	SCL		I ² C-BUS serial interface clock input connection.
#7	SDA	1/0	I ² C-BUS serial interface data input/output connection.
#8	Vcc	_	Supply Voltage

[mm] ■ Dimensions Model Code -3.2±0.15 Pin Connections ■ Recommended Land Pattern Pin No. Connection #1 OE(Output Enable) <Top View> INTN
N.C.
GND
Output
SCL K324SR l=1.00 > #3 ● D 701 SDA Vcc Function #1 Input | #5 Output condition Oscillation out High Z **4**.00>

