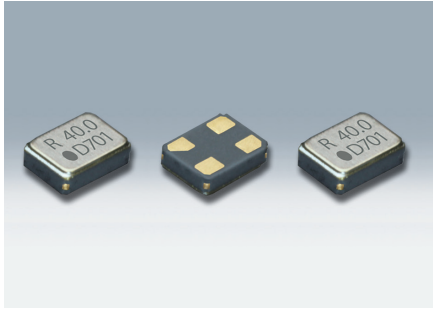


# SMD Crystal Oscillators (For Automotive)

## DSO1612AR



Actual size □

### ■ Features

- 3-state function
- Capable of operating over a wide temperature range, from  $-40$  to  $+125^{\circ}\text{C}$ .
- AEC-Q200 Compliant (Option: Equivalent to AEC-Q100)
- CMOS Level Output

### ■ Applications

- Multimedia devices such as car navigation systems and car audio
- Automotive camera

[Function Code]

DSO1612AR A Y

A : 3.3V  
M : 3.0V  
B : 2.8V  
C : 2.5V  
D : 1.8V

Y :  $\pm 100 \times 10^{-6}$   
Z :  $\pm 80 \times 10^{-6}$   
B :  $\pm 50 \times 10^{-6}$



### ■ Standard Specification

When requesting the product, please select the model and function code of your request.

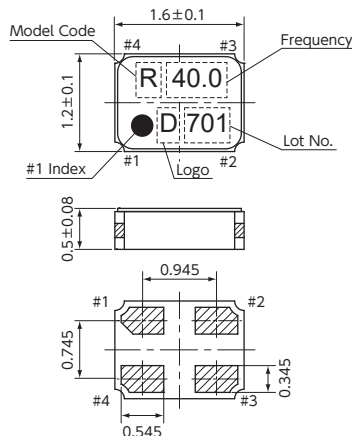
| Item  | Function Code                  |                     | Legend            | Output Frequency Range (MHz)   | Spec.                    |      |           | Unit             | Condition  |                           |
|---|--------------------------------|---------------------|-------------------|--------------------------------|--------------------------|------|-----------|------------------|--|---------------------------|
|   | Supply Voltage                 | Frequency tolerance |                   |                                | min.                     | typ. | max.      |                  |  |                           |
| Supply Voltage  | A                              | *                   | Vcc               | 0.584375 ≤ f <sub>o</sub> ≤ 80 | +3.0                     | +3.3 | +3.6      | V                |  |                           |
|   | M                              |                     |                   |                                | +2.7                     | +3.0 | +3.3      |                  |  |                           |
|   | B                              |                     |                   |                                | +2.6                     | +2.8 | +3.0      |                  |  |                           |
|   | C                              |                     |                   |                                | +2.25                    | +2.5 | +2.75     |                  |  |                           |
| Frequency Tolerance (Includes frequency tolerance at room temperature.) | *                              | Y                   | f_tol             | 0.584375 ≤ f <sub>o</sub> ≤ 80 | -100                     | -    | +100      | 10 <sup>-6</sup> | -40 to +125°C  |                           |
|   |                                | Z                   |                   |                                | -80                      | -    | +80       |                  | -40 to +110°C  |                           |
|   |                                | B                   |                   |                                | -50                      | -    | +50       |                  | -40 to +85°C   |                           |
| Current Consumption   | A,M                            | *                   | I <sub>cc</sub>   | 0.584375 ≤ f <sub>o</sub> < 40 | -                        | -    | +3.0      | mA               | No Load  |                           |
|   |                                |                     |                   | 40 ≤ f <sub>o</sub> < 80       | -                        | -    | +4.2      |                  |  |                           |
|   | B                              |                     |                   | 0.584375 ≤ f <sub>o</sub> < 40 | -                        | -    | +2.4      |                  |  |                           |
|   |                                |                     |                   | 40 ≤ f <sub>o</sub> ≤ 80       | -                        | -    | +3.7      |                  |  |                           |
|   | C                              |                     |                   | 0.584375 ≤ f <sub>o</sub> < 40 | -                        | -    | +2.0      |                  |  |                           |
|   |                                |                     |                   | 40 ≤ f <sub>o</sub> ≤ 80       | -                        | -    | +3.4      |                  |  |                           |
| D   | 0.584375 ≤ f <sub>o</sub> < 40 | -                   | -                 | +1.7                           |                          |      |           |                  |  |                           |
| 40 ≤ f <sub>o</sub> ≤ 80  | -                              | -                   | +2.7              |                                |                          |      |           |                  |  |                           |
| Stand-by Current (#1 pin "L" level)                                     | *                              | *                   | I <sub>std</sub>  | *                              | -                        | -    | +20       | μA               |  |                           |
| Load Condition  | *                              | *                   | L <sub>CMOS</sub> | *                              | -                        | -    | 15        | pF               |  |                           |
| Symmetry  | *                              | *                   | SYM               | *                              | 40                       | 50   | 60        | %                | at 50% Vcc   |                           |
| 0 Level Output Voltage  | *                              | *                   | VoL               | *                              | -                        | -    | Vcc × 0.1 | V                |  |                           |
| 1 Level Output Voltage  | *                              | *                   | V <sub>OH</sub>   | *                              | Vcc × 0.9                | -    | -         | V                |  |                           |
| Rise and Fall Time  | A,M,B,C                        | *                   | tr, tf            | *                              | -                        | -    | 3.0       | ns               | 10 to 90% Vcc Level  |                           |
|   | D                              |                     |                   |                                | -                        | -    | 5         |                  |  |                           |
| OE Pin 0 Level Input Voltage  | *                              | *                   | V <sub>IL</sub>   | *                              | -                        | -    | Vcc × 0.2 | V                |  |                           |
| OE Pin 1 Level Input Voltage  | *                              | *                   | V <sub>IH</sub>   | *                              | Vcc × 0.8                | -    | -         | V                |  |                           |
| Output Disable Time   | *                              | *                   | t <sub>PLZ</sub>  | *                              | -                        | -    | 200       | ns               |  |                           |
| Output Enable Time  | *                              | *                   | t <sub>PZL</sub>  | *                              | -                        | -    | 2         | ms               |  |                           |
| Period Jitter (1)   | *                              | *                   | t <sub>RMS</sub>  | *                              | -                        | 2.2  | -         | ps               | σ  |                           |
|   |                                |                     | tp-p              | *                              | -                        | 20   | -         | ps               | Peak to peak   |                           |
| Total Jitter (1)  | *                              | *                   | t <sub>TL</sub>   | *                              | -                        | 31   | -         | ps               | t <sub>DJ</sub> +n×t <sub>RJ</sub> n=14.1 (BER=1×10 <sup>-15</sup> ) (2) |                           |
| Phase Jitter  | *                              | *                   | tpj               | *                              | 40 ≤ f <sub>o</sub> ≤ 80 | -    | -         | 1                | ps   | fo offset: 12kHz to 20MHz |
| Reliability   | AEC-Q100/AEC-Q200              |                     |                   |                                |                          |      |           |                  |  |                           |
| Packing Unit (3)  | 3000pcs./reel (φ 180)          |                     |                   |                                |                          |      |           |                  |  |                           |

- (1) Measured WAVECREST DTS-2075
- (2) t<sub>DJ</sub>:Deterministic jitter t<sub>RJ</sub>:Random jitter
- (3) Moisture prevention packing is unnecessary. Moisture Sensitivity Level:Level 1 (IPC/JEDEC J-STD-033)

Consult our sales representative for other specifications.

[mm]

### ■ Dimensions



### ■ Recommended Land Pattern

| Pin Connections |                    |
|-----------------|--------------------|
| Pin No.         | Connection         |
| #1              | OE (Output Enable) |
| #2              | GND                |
| #3              | Output             |
| #4              | Vcc                |

| Function |                     |
|----------|---------------------|
| #1 Input | #3 Output condition |
| H        | Oscillation out     |
| Open     | Oscillation out     |
| L        | High Z              |

