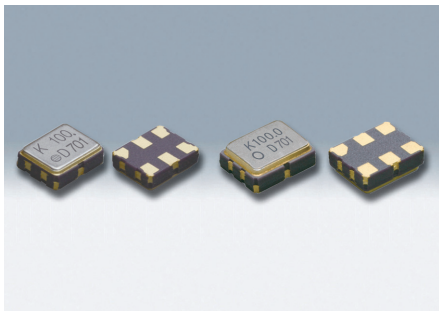
SMD Differential Output Crystal Oscillators

DSO223SK/DSO323SK/DSO223SJ/DSO323SJ/DSO223SD/DSO323SD



Actual size DSO223S DSO323S

■ Features

- 2.5V/3.3V operating voltage, High speed type
- 3-state function
- LV−PECL output (DSO223/323SK)
- LVDS output (DSO223/323SJ)
- HCSL output (DSO223/323SD)
- DSO223SK/SJ/SD: AEC-Q200 Compliant
  DSO323SK/SJ/SD: AEC-Q200 Compliant (Option: Equivalent to AEC-Q100)

Pb-Free

RoHS Compliant

■ Applications

- Sever, Optical transmission device, Communication base station and Automotive multimedia device

[Type]

DSO223S SERIES	2520 size
DSO323S SERIES	3225 size

[Function Code]

DSO***S K A A A

- Model Code: K : LVPECL / J : LVDS / D : HCSL
- Supply Voltage: A : 3.3V / C : 2.5V
- Operating Temperature Range: A : − 10 to +70℃ / C : − 40 to +85℃
- Frequency Tolerance: A : ±100×10^{-6} / B : ±50×10^{-6}

■ Standard Specification

When requesting the product, please select the model and function code of your request.

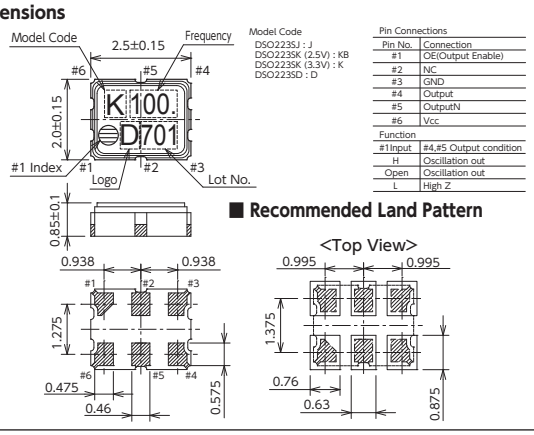
Item \ Type	Legend	DSO223SK DSO323SK	DSO223SJ DSO323SJ	DSO223SD DSO323SD
Output Specification	–	LV-PECL	LVDS	HCSL
Output Frequency Range	f_0	13.5 to 167MHz (DSO223S SERIES) /13.5 to 212.5MHz (DSO323S SERIES)		
Supply Voltage	V_{CC}	+2.5V±0.125V/+3.3V±0.165V		
Frequency Tolerance (Includes frequency tolerance at room temperature.)	f_tol	±50×10^{-6} max., ±100×10^{-6} max.		
Storage Temperature Range	T_stg	−40 to +85℃		
Operating Temperature Range	T_use	−10 to +70℃ ,−40 to +85℃		
Current Consumption	I_{CC}	45mA max. (f_0≦170MHz), 50mA max. (170MHz<f_0≦212.5MHz)	20mA max.	30mA max. (f_0≦170MHz), 35mA max. (170MHz<f_0≦212.5MHz)
Stand-by Current (#1 pin "L" Level)	I_std	10μA max.		
Load Condition	Load-R	50Ω to V_{CC}−2V	100Ω (Output−OutputN)	50Ω
Symmetry	SYM	45 to 55% [at outputs cross point]		
0 Level Output Voltage	V_{OL}	V_{CC}−1.81 to V_{CC}−1.62V	–	−0.15 to 0.15V
1 Level Output Voltage	V_{OH}	V_{CC}−1.025 to V_{CC}−0.88V	–	0.58 to 0.85V
Rise and Fall Time	tr, tf	0.5ns max. [20 to 80% Output, OutputN]	0.4ns max. [20 to 80% Output−OutputN]	0.5ns max. [0.175 to 0.525V Level]
Differential Output Voltage	V_{OD1}, V_{OD2}	–	0.247 to 0.454V	–
Change to V_{OD}	ΔV_{OD}	–	50mV [ΔV_{OD}= | V_{OD1}−V_{OD2} |]	–
Offset Voltage	V_{OS}	–	1.125 to 1.375V	–
Offset to V_{OS}	ΔV_{OS}	–	50mV	–
Crossing Point Voltage	Vcr	–	–	250 to 550mV
OE Pin 0 Level Input Voltage	V_{IL}	V_{CC}×0.3 max.		
OE Pin 1 Level Input Voltage	V_{IH}	V_{CC}×0.7 min.		
Output Disable Time	tPLZ	200ns		
Output Enable Time	tPZL	2ms		
Period Jitter (1)	tRMS	5ps typ. (13.5MHz≦f_0<27MHz) / 2.5ps typ. (27MHz≦f_0≦212.5MHz) (σ)		
	tp-p	33ps typ. (13.5MHz≦f_0<27MHz) / 22ps typ. (27MHz≦f_0≦212.5MHz) (Peak to peak)		
Total Jitter (1)	tTL	50ps typ. (13.5MHz≦f_0<27MHz) / 35ps typ. (27MHz≦f_0≦212.5MHz) [tDJ + n×tRJ n=14.1(BER=1×10^{-12}) (2)]		
Phase Jitter	tpj	1.5ps max. (13.5MHz≦f_0<27MHz) / 1ps max. (27MHz≦f_0≦212.5MHz) [13.5MHz≦f_0<40MHz, f_0 offset:12kHz to 5MHz f_0≧40MHz, f_0 offset:12kHz to 20MHz]		
Packing Unit (3)	–	2000pcs./reel (φ180)		

(1) Measured WAVECREST DTS-2075
(2) tDJ : Deterministic jitter tRJ : Random jitter
(3) Moisture prevention packing is unnecessary.
Moisture Sensitivity Level : Level 1 (IPC/JEDEC J-STD-033)

Consult our sales representative for other specifications.

■ DSO223S SERIES [mm]

■ Dimensions



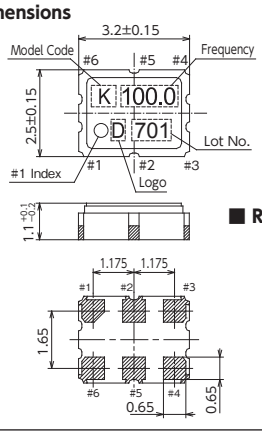
Model Code
DSO223SJ : J
DSO223SK (2.5V) : KB
DSO223SK (3.3V) : K
DSO223SD : D

Pin Connections	
Pin No.	Connection
#1	OE(Output Enable)
#2	NC
#3	GND
#4	Output
#5	OutputN
#6	Vcc
Function	
#1 Input	#4,#5 Output condition
H	Oscillation out
Open	Oscillation out
L	High Z

■ Recommended Land Pattern

■ DSO323S SERIES [mm]

■ Dimensions



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DSO323SK(3.3V):K
DSO323SD:D

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