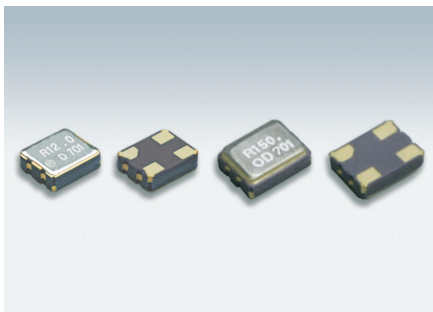


# SMD Crystal Oscillators (For Automotive)

## DSO221SR/DSO321SR



Actual size DSO221SR □ DSO321SR □

### Features

- 3-state function
- Capable of operating over a wide temperature range, from -40 to +125°C.
- AEC-Q200 Compliant (Option: Equivalent to AEC-Q100)
- CMOS Level Output

### Applications

- Multimedia devices such as car navigation systems and car audio
- Automotive camera

[Type]

DSO221SR	2520 size
DSO321SR	3225 size

[Function Code]

DSO\*\*\*SR A A

A : 3.3V	$\begin{matrix} \uparrow \\ \uparrow \\ \uparrow \\ \uparrow \end{matrix}$	A, Y : $\pm 100 \times 10^{-6}$
M : 3.0V		Z : $\pm 80 \times 10^{-6}$
B : 2.8V		B : $\pm 50 \times 10^{-6}$
C : 2.5V		
D : 1.8V		



### Standard Specification

When requesting the product, please select the model and function code of your request.

Item	Function Code		Output Frequency Range (MHz)	Legend	Spec.				Condition
	Supply Voltage	Frequency tolerance			min.	typ.	max.	Unit	
Supply Voltage	A	*	$0.2 \leq f_0 \leq 125$	V <sub>CC</sub>	+3.0	+3.3	+3.6	V	
	M		$0.2 \leq f_0 \leq 125$		+2.7	+3.0	+3.3		
	B		$0.2 \leq f_0 \leq 100$		+2.6	+2.8	+3.0		
	C		$0.2 \leq f_0 \leq 100$		+2.25	+2.5	+2.75		
	D		$0.2 \leq f_0 \leq 80$		+1.6	+1.8	+2.0		
Frequency Tolerance (Includes frequency tolerance at room temperature.)	*	Y	$0.2 \leq f_0 \leq 100$	f <sub>tol</sub>	-100	-	+100	×10 <sup>-6</sup>	-40 to +125°C
		Z	$0.2 \leq f_0 \leq 100$		-80	-	+80		-40 to +110°C
		A	$100 < f_0 \leq 125$		-100	-	+100		
		B	$0.2 \leq f_0 \leq 100$		-50	-	+50		-40 to +85°C
Current Consumption	A, M	*	$0.2 \leq f_0 < 54$	I <sub>CC</sub>	-	-	+4.0	mA	No Load
			$54 \leq f_0 < 80$		-	-	+6.0		
			$80 \leq f_0 \leq 125$		-	-	+8.0		
	B	*	$0.2 \leq f_0 < 54$		-	-	+3.5		
			$54 \leq f_0 < 80$		-	-	+5.5		
			$80 \leq f_0 \leq 100$		-	-	+7.5		
	C	*	$0.2 \leq f_0 < 54$		-	-	+3.0		
			$54 \leq f_0 < 80$		-	-	+5.0		
			$80 \leq f_0 \leq 100$		-	-	+7.0		
	D	*	$0.2 \leq f_0 < 54$		-	-	+2.5		
			$54 \leq f_0 < 80$		-	-	+4.5		
					-	-			
Stand-by Current (#1 pin 'L' level)	*	*	*	L <sub>std</sub>	-	-	+10	μA	
Load Condition	*	*	*	L <sub>CMOS</sub>	-	-	15	pF	
Symmetry	*	*	*	SYM	40	50	60	%	50% V <sub>CC</sub> Level
0 Level Output Voltage	*	*	*	V <sub>OL</sub>	-	-	V <sub>CC</sub> × 0.1	V	
1 Level Output Voltage	*	*	*	V <sub>OH</sub>	V <sub>CC</sub> × 0.9	-	-	V	
Rise and Fall Time	*	*	$0.2 \leq f_0 \leq 54$	tr,tf	-	-	8	ns	10 to 90% V <sub>CC</sub> Level
			$54 < f_0 < 100$		-	-	4		
			$100 \leq f_0 \leq 125$		-	-	3		
OE Pin 0 Level Input Voltage	*	*	*	V <sub>IL</sub>	-	-	V <sub>CC</sub> × 0.2	V	
OE Pin 1 Level Input Voltage	*	*	*	V <sub>IH</sub>	V <sub>CC</sub> × 0.8	-	-	V	
Output Disable Time	*	*	*	t <sub>PLZ</sub>	-	-	150	ns	
Output Enable Time	*	*	*	t <sub>PZL</sub>	-	-	5	ms	
Period Jitter (1)	*	*	*	t <sub>RMS</sub>	-	2.2	-	ps	σ
Total Jitter (1)	*	*	*	tp-p	-	20	-	ps	Peak to peak
Phase Jitter	*	*	*	t <sub>TL</sub>	-	31	-	ps	t <sub>DJ</sub> +n×t <sub>RJ</sub> n=14.1 (BER=1×10 <sup>-13</sup> ) (2)
									fo offset: 12kHz to 20MHz
Reliability	AEC-Q100/AEC-Q200								
Packing Unit (3)	2000pcs./reel(180 φ)								

- (1) Measured WAVECREST DTS-2075
- (2) t<sub>DJ</sub>:Deterministic jitter t<sub>RJ</sub>:Random jitter
- (3) Moisture prevention packing is unnecessary. Moisture Sensitivity Level:Level 1 (IPC/JEDEC J-STD-033)

Consult our sales representative for other specifications.

### DSO221SR

[mm]

### DSO321SR

[mm]

#### Dimensions

Pin No.	Connection
#1	OE(Output Enable)
#2	GND
#3	Output
#4	V <sub>CC</sub>

#1 Input	#3 Output condition
H	Oscillation out
Open	Oscillation out
L	High Z

#### Dimensions

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