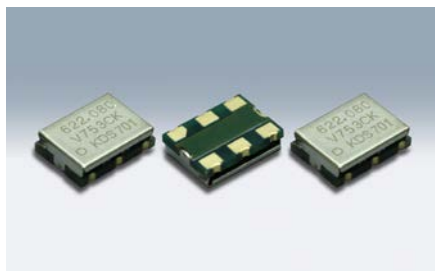


SMD Differential Output Voltage Controlled Crystal Oscillators

DSV753CK/DSV753CJ



Actual size

■ Features

- Available with two types of output functions: LV-PECL, and LVDS
- Differential output (LV-PECL, LVDS)
- Low jitter
- Available up to 700MHz by using AT cut fundamental resonator.

■ Applications

- Optical transmission device, radio transmitter-receiver equipment, digital image devices, and HDTV related equipment.



■ Standard Specification

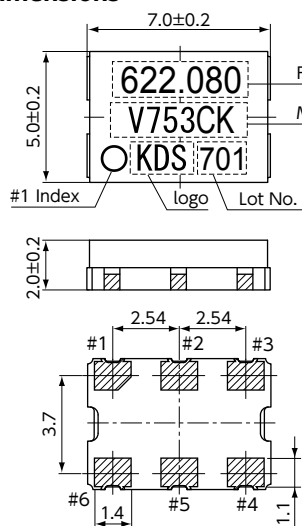
Item	Type	Legend	DSV753CK	DSV753CJ
Output Specification	—		LV-PECL	LVDS
Output Frequency Range	f ₀		350 to 700MHz	
Supply Voltage	V _{CC}		+3.3V±0.165V	
Frequency Control Voltage	V _{cont}		+1.65V±1.65V	
Storage Temperature Range	T _{stg}		-40 to +85°C	
Operating Temperature Range	T _{use}		-20 to +70°C	
Frequency Tolerance (Includes frequency tolerance at room temperature.)	f _{tol}		±50×10 ⁻⁶ max.	
Frequency Adjustment Range	f _{cont}		±100×10 ⁻⁶ min. Positive Slope	
Current Consumption	I _{CC}		80mA max.	60mA max.
Output Load	Load		50Ω to V _{CC} -2V	100Ω(Output-OutputN)
Symmetry	SYM		45 to 55%[at outputs cross point]	
0 Level Output Voltage	V _{OL}		2.155 to 2.405V	—
1 Level Output Voltage	V _{OH}		1.355 to 1.700V	—
Rise and Fall Time	tr, tf		0.4ns max.[20 to 80% Output,OutputN]	0.4ns max.[20 to 80% Output-OutputN]
Differential Output Voltage	V _{OD1} , V _{OD2}		—	0.247 to 0.454V
Change to V _{OD}	ΔV _{OD}		—	50mV[ΔV _{OD} = V _{OD1} -V _{OD2}]
Offset Voltage	V _{OS}		—	1.125 to 1.375V
Offset to V _{OS}	ΔV _{OS}		—	100mV
Phase Jitter	tpj		1ps max. (f ₀ offset:12kHz to 20MHz)	
Packing Unit (1)	—		100pcs. / reel or 500pcs. / reel (φ180)	

(1) Moisture prevention packing is unnecessary.
Moisture Sensitivity Level : Level 1 (IPC/JEDEC J-STD-033)

Consult our sales representative for other specifications.

[mm]

■ Dimensions



Model Code DSV753CK→V753CK
DSV753CJ→V753CJ

Pin No.	Connection
#1	V _{cont}
#2	NC
#3	GND
#4	Output
#5	OutputN - DSV753CK DSV753CJ
#6	V _{CC}

■ Recommended Land Pattern

<Top View>

