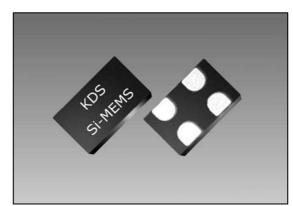
Low Power MEMS Oscillator



MO1602



Features

- 52 standard frequencies between 3.57 MHz and 77.76 MHz
 Industry-standard packages:
 - 2.0 x 1.6, 2.5 x 2.0, 3.2 x 2.5, 5.0 x 3.2, 7.0 x 5.0 mm
- \bullet Excellent total frequency stability as low as ±20 x 10⁻⁶
- ●Low power consumption of +3.5 mA typical at f = 20 MHz, Vdd = +1.8V ■Applications
- ●Ideal for DSC, DVC, DVR, IP CAM, Tablets, e-Books, SSD, GPON, EPON etc.
- ●Ideal for high-speed serial protocols such as: USB, SATA, SAS, Firewire, 100M/1G/10G Ethernet, etc.



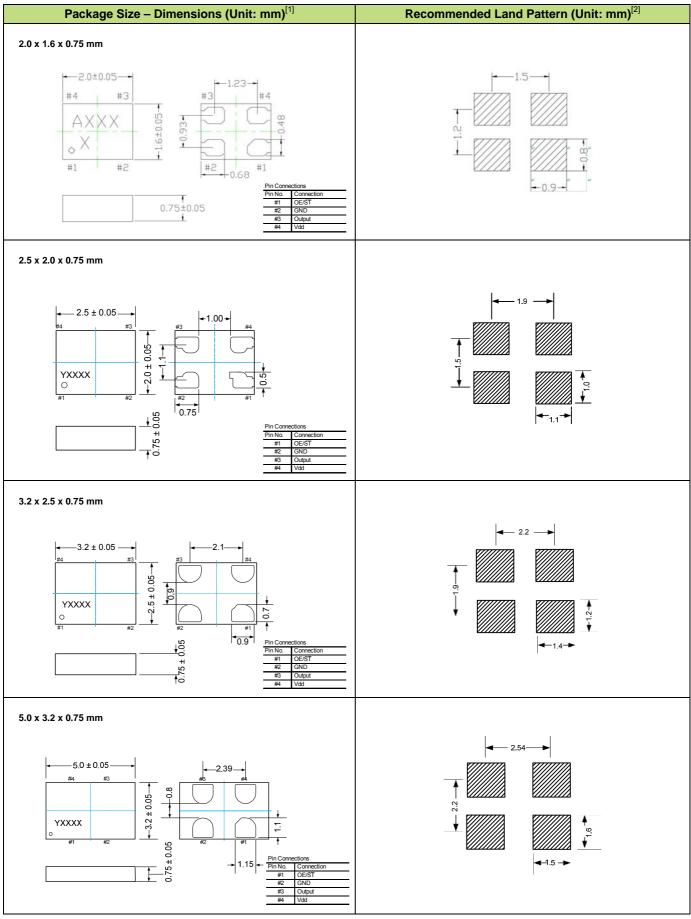
Item	symbol	Min.	Тур.	Max.	Unit	Condition
Output Frequency Range	f	3.57	-	77.76	MHz	Refer to datasheet for exact list of supported frequencies
Supply Voltage	Vdd	+1.62 +2.25 +2.52 +2.7 +2.97 +2.97 +2.25	+1.8 +2.5 +2.8 +3.0 +3.3 -	+1.98 +2.75 +3.08 +3.3 +3.63 +3.63	V	
Operating Temperature Range	T_use	-20 -40		+70 +85	°C	Extended Commercial Industrial
Frequency Stability	F_stab	-20 -25 -50		+20 +25 +50	x10⁻ ⁶	Inclusive of initial tolerance at +25°C, 1st year aging at +25°C, and variations over operating temperature, rated power supply voltage and load.
Current Consumption	ldd	_ _ _	+3.8 +3.7 +3.5	+4.5 +4.2 +4.1	mA	No load condition, f = 20 MHz, Vdd = +2.8V to +3.3V No load condition, f = 20 MHz, Vdd = +2.5V No load condition, f = 20 MHz, Vdd = +1.8V
OE Disable Current	I_od	-	-	+4.2 +4.0	mA	Vdd = +2.5V to +3.3V, OE = GND, output is Weakly Pulled Down Vdd = 1.8V, OE = GND, output is Weakly Pulled Down
Standby Current	I_std		+2.6 +1.4 +0.6	+4.3 +2.5 +1.3	μA	\overline{ST} = GND, Vdd = +2.8V to +3.3V, Output is weakly pulled down \overline{ST} = GND, Vdd = +2.5V, Output is weakly pulled down \overline{ST} = GND, Vdd = +1.8V, Output is weakly pulled down
Duty Cycle	DC	45	-	55	%	All Vdds
Output Low Voltage	V _{OL}	-	_	Vdd x 0.1	V	$I_{OL} = +4.0 \text{ mA (Vdd} = +3.0 \text{ vor } +3.3 \text{V})$ $I_{OL} = +3.0 \text{ mA (Vdd} = +2.8 \text{V and Vdd} = +2.5 \text{V})$ $I_{OL} = +2.0 \text{ mA (Vdd} = +1.8 \text{V})$
Output High Voltage	V _{OH}	Vdd x 0.9	-	-	V	I _{OH} = -4.0 mA (Vdd = +3.0V or +3.3V) I _{OH} = -3.0 mA (Vdd = +2.8V and Vdd = +2.5V) I _{OH} = -2.0 mA (Vdd = +1.8V)
Rise/Fall Time	Tr,Tf		1.0 1.3 -	2.0 2.5 2.0	ns	Vdd = +2.5V, +2.8V, +3.0V or +3.3V, 20% - 80% Vdd = +1.8V, 20% - 80% Vdd = +2.25V - +3.63V, 20% - 80%
Input Low Voltage	V _{IL}	-	-	Vdd x 0.3	V	Pin 1, OE or ST
Input High Voltage	V _{IH}	Vdd x 0.7	-	-	V	Pin 1, OE or ST
Startup Time	T_start	-	-	5.0	ms	Measured from the time Vdd reaches its rated minimum value
Enable/Disable Time	T_oe	-	-	138	ns	f = 77.76 MHz. For other frequencies, T_oe = 100 ns + 3 * cycles
Resume Time	T_resume	-	-	5.0	ms	Measured from the time ST pin crosses 50% threshold
RMS Period Jitter	T_jitt	-	1.8 1.8	3.0 3.0	ps	f = 75 MHz, Vdd = +2.5V, +2.8V, +3.0V or +3.3V f = 75 MHz, Vdd = +1.8V
Peak-to-peak Period Jitter	T_pk	-	12 14	25 30	ps	f = 75 MHz, Vdd = +2.5V, +2.8V, +3.0V or +3.3V f = 75 MHz, Vdd = +1.8V
RMS Phase Jitter (random)	T_phj		0.5 1.3	0.9 2.0	ps	f = 75 MHz, Integration bandwidth = 900 kHz to 7.5 MHz f = 75 MHz, Integration bandwidth = 12 kHz to 20 MHz

Consult our sales representative for other specifications.



MO1602

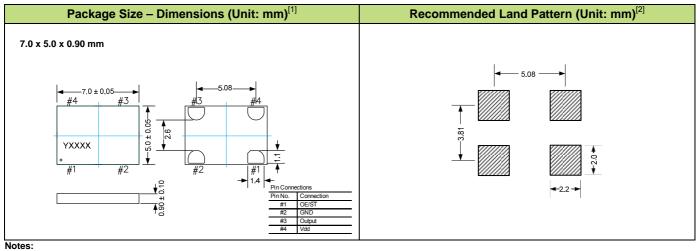
Dimensions and Patterns





MO1602

Dimensions and Patterns



Top marking: Y denotes manufacturing origin and XXXX denotes manufacturing lot number. The value of "Y" will depend on the assembly location of the device.
 A capacitor of value 0.1 μF between Vdd and GND is required