

Voltage Controlled MEMS Oscillators (VCMO)

MO3807/MO3808/MO3809



■Features

- Frequency Tolerance as tight as ±25×10⁻⁶
- Widest pull range options: ±25 to ±1600×10⁻⁶
- Industry-Standard packages: 2.7×2.4 mm (4-pin, compatible with 2.5×2.0 mm footprint) 3.2×2.5 mm (4-pin), 5.0×3.2 mm (6-pin), 7.0×5.0 mm (6-pin)





RoHS Compliant

Applications

- Telecom clock synchronization, instrumentation
- Low bandwidth analog PLL, jitter cleaner, clock recovery, audio
- Video, 3G/HD-SDI, FPGA, broadband and networking

Model	Output Frequency (MHz)	Frequency Tolerance (×10 ⁻⁶)	Supply Voltage (V)	Current Consumption (mA Typ.)	Size (mm)	Output
MO3807	30 standard frequencies	±25,±50	+1.71 to +1.89.	+29 to +36 (+10 μA stby)	2.7×2.4×0.8, 3.2×2.5×0.8, 5.0×3.2×0.8, 7.0×5.0×1.0 (QFN)	LVCMOS
MO3808	1 to 80	±10.±25.±50	+2.25 to +3.63			
MO3809	80 to 220	110,125,150				

■ Standard Specification (MO3808)

■ Standard Specification (MOS606)										
Item	Legend	Min.	Тур.	Max.	Unit	Condition				
Output Frequency Range	f	1	-	80	MHz					
	Vdd	+1.71	+1.8	+1.89	V	Additional supply voltages between +2.5V and +3.3V can be supported.				
Supply Voltage		+2.25	+2.5	+2.75						
Supply Vollage		+2.52	+2.8	+3.08						
		+2.97	+3.3	+3.63						
Operating Temperature	T_use	-20	-	+70	$^{\circ}$	Extended Commercial				
Range		-40	-	+85		Industrial				
	F_stab	-10	-	+10	×10 ⁻⁶	Inclusive of Initial tolerance[4] at +25° C and variation over temperature, rated supply voltage and load.				
Frequency Stability		-25	-	+25						
		-50	-	+50						
10-year Aging	F_aging10	-5.0	-	+5.0	×10 ⁻⁶	10 years, T _A = +25℃				
Current Consumption	ldd	-	+31	+33	mA	No load condition, $f = 20$ MHz, $Vdd = +2.5V$, $+2.8V$ or $+3.3V$				
Current Consumption		-	+29	+31		No load condition, f = 20 MHz, Vdd = +1.8V				
Standby Comment	l_std	-	-	+70	μΑ	Vdd = +2.5V, $+2.8V$, $+3.3V$, $ST = GND$, Output is weakly pulled down				
Standby Current		-	-	+10		$Vdd = +1.8V$, $\overline{ST} = GND$, Output is weakly pulled down				
Duty Cycle	DC	45	-	55	%	All Vdds				
, ,	V _{OL}	-	-	Vdd×0.1	V	$I_{OL} = +7.0 \text{ mA (Vdd} = +3.0 \text{V or } +3.3 \text{V)}$				
Output Low Voltage						$I_{OL} = +4.0 \text{ mA (Vdd} = +2.8 \text{V or } +2.5 \text{V)}$				
						$I_{OL} = +2.0 \text{ mA (Vdd} = +1.8 \text{V)}$				
Output High Voltage	V _{OH}	Vdd×0.9	-	-	V	$I_{OH} = -7.0 \text{ mA (Vdd} = +3.0 \text{V or } +3.3 \text{V)}$ $I_{OH} = -4.0 \text{ mA (Vdd} = +2.8 \text{V or } +2.5 \text{V)}$				
Output High Voltage						$I_{OH} = -2.0 \text{ mA } (Vdd = +2.8V)$ $I_{OH} = -2.0 \text{ mA } (Vdd = +1.8V)$				
Rise and Fall Time	Tr, Tf	-	1.5	2.0	ns	Vdd = +1.8V, +2.5v, +2.8V or +3.3V, 10% to 90% Vdd level				
Pull Paras IF (1	PR	±25, ±50	. ±100. ±1	150, ±200,						
Pull Range [5,6]), ±800, ±	1600,		See the Absolute Pull Range and APR table of datasheet				
	VC_U	+1.7	-	_	V	Vdd = +1.8V, Voltage at which maximum deviation is guaranteed.				
Upper Control Voltage		+2.4	-	-		Vdd = +2.5V, Voltage at which maximum deviation is guaranteed.				
opper control voltage		+2.7	-	-		Vdd = +2.8V, Voltage at which maximum deviation is guaranteed.				
		+3.2	-	-		Vdd = +3.3V, Voltage at which maximum deviation is guaranteed.				
Lower Control Voltage	VC_L	-	-	+0.1	V	Voltage at which minimum deviation is guaranteed.				
Control Voltage Input Impedance	Z_in	100	-	-	kΩ					
Control Voltage Input Capacitance	C_in	-	5.0	-	pF					
Linearity	Lin	-	0.1	1.0	%					
Frequency Change Polarity	-	Positive slope		e	-					
Start-up Time	T_start	-	-	10	ms					
Enable and Disable Time	T_oe	-	-	180	ns	f = 40MHz, all Vdds, For other freq., T_oe = 100 ns + 3 clock periods				
Resume Time	T_resume	-	7.0	10	ms					
DAAC Deviced litter	T_jitt	-	1.5	2.0	ps	f = 20 MHz, Vdd = +2.5V, +2.8V or +3.3V				
RMS Period Jitter		-	2.0	3.0		f = 20 MHz, Vdd = +1.8V				
RMS Phase Jitter (random)	T_phj	-	0.5	1.0	ps	f = 20 MHz, Integration bandwidth = 12 kHz to 20 MHz, All Vdds				
Packing Unit	1000pcs./reel (\$\phi\$180) or 3000pcs./reel (\$\phi\$180: 2724, 3225 package)									

- [1]. All electrical specifications in the above table are specified with 15 pF output load and for all Vdd(s) unless otherwise stated.
- [2]. The typical value of any parameter in the Electrical Characteristics table is specified for the nominal value of the highest voltage option for that parameter and at +25 °C temperature.
- [3]. All max and min specifications are guaranteed across rated voltage variations and operating temperature ranges, unless specified otherwise. [4]. Initial tolerance is measured at Vin = Vdd/2.
- [5]. Absolute Pull Range (APR) is defined as the guaranteed pull range over temperature and voltage. [6]. APR = pull range (PR) frequency tolerance (F_stab) Aging (F_aging)